



PATENT APPLICATION

Sheet 1 of 1

FORM PTO 1449		ATTY. DOCKET NO. 200313752-1		APPLICATION NO. 10/758,368		CONFIRMATION NO. 6097	
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)		APPLICANT Simon C. Steely, Jr.					
		FILING DATE January 15, 2004				GROUP 2189	
REFERENCE DESIGNATION U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear		
	1A	5819105	10-6-1998	Moriarty, et al.			
	1B	6546465	4-8-2003	Bertone			
	1C	6356918	3-12-2002	Chuang et al.			
	1D	6633960	10-14-2003	Kessler et al.			
	1E	6766360	07-2004	Conway et al.			
	1F	6523138	02-2003	Natsume et al.			
	1G	6961827	11-2005	Shanahan et al.			
	1H						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached	
	1B						
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)							
		Jim Handy, The Cache Memory Book, 1993; Page 153 line 29 to page 154 line 24; page 169.					
		Rajeev, Joshi et al., "Checking Cache-Coherency Protocols with TLA+", Journal of Formal Methods in System Design. Vol. 22, No. 2, March 2003, pp. 125-131, Springer, Netherlands.					
		Acacio, Manuel E. et al, "Owner Prediction for Accelerating Cache-to-Cache Transfer Misses in cc-NUMA Multiprocessors," SC2002 High Performance Networking and Computing, pp. 49-60, November 2002, IEEE Computer Society Press, Los Alamitos, CA.					
EXAMINER				DATE CONSIDERED			

Rev 4/04 (PTO 1449)

WAS:156222.1